

WORKSHOP AGENDA

Scale up: Building a State-of -the Art Enterprise Supercomputer

Date: March 8, 2006 **Time:** 9:00 a.m. – 1:00 p.m. **Where:** Washington Convention Center **Room:** 144C- ground level

Times	Topic	Speakers
9:00 a.m. Length: 15 min	Introduction Welcome – special gift distribution to all attendees	APPRO Anthony Kenisky, Sales Director of APPRO
9:15 a.m. Length: 20 min	Accelerating Performance with Clusters - The Problem – Scale up from 0 to 40TF - Key Considerations: Processor, Memory and I/O Performance	Raytheon Mr. Jim Ballew HPC Chief Architect, Raytheon Intelligence & Information Systems
9:35 a.m. Length: 20 min	Processor Performance - Why AMD - Dual-Core Technology: Memory controller & Hyper-Transport - Current and Future Performance	AMD Ramesh Joginpali Strategic Programs Manager
9:55 a.m. Length: 20 min	I/O Performance - Why Infiniband - Latency and Bandwidth - SDR & DDR	Mellanox Marc Sultzbaugh Director of High-Performance computing
10:15 a.m. Length: 20 min	Balanced Blade Architecture - Why Appro Blade technology - Bringing CPU/Memory I/O together - Fabric (2-D) and Scalability - Power, Heat and Footprint	APPRO Adrian Wu HPC Solution Architect
10:35 a.m. Length: 20 min	The Storage Challenge - Infiniband Native Devices - Parallel Compute Requires Parallel I/O - Bandwidth vs. Expectations	DataDirect Networks Mr. Randy Kreiser Chief Storage Architect
10:55 a.m. Length: 30 min	A Complete Platform – Bringing it all Together - Operating a 40TF platform - Provisioning and Scheduling - Parallel Application Performance	Raytheon Mr. Jim Ballew HPC Chief Architect, Raytheon Intelligence & Information Systems
11:30 a.m.	Q & A and Open Discussion	All
11:45 a.m. – 1pm	Lunch	